UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 243650US2S

First Inventor or Application Identifier Naoyuki TAMURA, et al.

Tradyaki 17 tillot 0 t, ct al.

Title METHOD FOR COMPRESSING SEMICONDUCTOR INTEGRATED CIRCUIT, USING DESIGN REGION DIVIDED INTO PLURAL BLOCKS

APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents							Commissioner for Patents ADDRESS TO: Mail Stop Patent Application Alexandria, Virginia 22313					РТО 36	
1.	1. Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)				ACCOMPANYING APPLICATION PARTS							U.S.	
					7.		Assignm	ent Pape	ers (cover sh	neet & de	ocument(s))	88	
2.		Specification	Total Sheets	37	8.		Applicati	ion Data	Sheet. See	37 CFR	1.76	031088	
			_		9.		37 C.F.F (when then	R. §3.73(I re is an assi	b) Statemen ^(gnee)	t 🗆	Power of Attorney	ŏ	
3.		Drawing(s) (35 U.S.C. 113)	Total Sheets	15	10.		English 1	Translatio	on Documer	nt <i>(if app</i>	licable)		
					11.			ion Disclent (IDS)/	osure PTO-1449		Copies of IE Citations (1)	os)	
4.		Oath or Declaration	Total Pages		12.		Prelimin	ary Amer	ndment				
	a.	☐ Newly executed (origin	al or copy)		13.		White A	dvance S	erial No. Po	stcard		ľ	
	b. Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 17 completed)						14. Certified Copy of Priority Document(s) (1) (if foreign priority is claimed)						
		 DELETION OF II Signed statement atta the prior application, s 1.33(b). 	ched deleting inven		15.		Applican See 37 CF		small entity	status.			
5.		CD-ROM or CD-R in duplica Program (Appendix)	ite, large table o	or Computer	16.		Other:	Req	uest for Prior	ity			
6.		Nucleotide and/or Amino Ac	id Sequence Su	ubmission	1							ľ	
٠.	а.	(if applicable, all necessary, ☐ Computer Readable For											
	b.	Specification or Sequence L											
	٥.	i. CD-ROM or CD-R (2	-										
		ii. □ Paper											
	C.	☐ Statements verifying ide	entity of above o	copies								l	
17.	lf a	CONTINUING APPLICATION			lv the re	auis	ite informat	tion below:					
		Continuation		Continuation					application	no.:			
Prior application information: Examiner: Group Art Unit:													
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.									n				
18. CORRESPONDENCE ADDRESS													
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Registration Number 21,124



Docket No.

243650US2S

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Naoyuki TAMURA, et al.

SERIAL NO:

New Application

FILING DATE: Herewith

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

FOR:

METHOD FOR COMPRESSING SEMICONDUCTOR INTEGRATED CIRCUIT, USING DESIGN

REGION DIVIDED INTO PLURAL BLOCKS

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FEE TRANSMITTAL

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The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to Dep Account No. 15-0030. A duplicate copy of this sheet is enclosed.									
			Respectfully Submitted,						
			OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.						
Date: 10/6/03		6/03	G/mn Mouland						
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22850			Registration Number 21,124						

A duplicate copy of this sheet is enclosed.